Call For Papers (CFP) - Following the very successful and well-attended SECRISC-V'20 and SECRISC-V'21, the *Secure RISC-V (SECRISC-V) Architecture Design Exploration Workshop* seeks original research papers on the design, implementation, verification, and evaluation of micro-architecture security features, hardware-assisted security techniques, and secure executions around the RISC-V instruction set architecture (ISA).

The Theme for this year's workshop is *Secure RISC-V for the Embedded World*.

The Workshop will serve as the anchor to the in-person portion of the ESWEEK in Phoenix, Arizona, Thursday October 13th, 2022.

Website: https://secriscv.org/

Topics
Submission of early work is encouraged. The RISC-V ISA based topics of specific interest for the workshop include, but are not limited to:

- Secure cores and multicores
- ISA extensions for Security
- Software and hardware obfuscation Techniques
- Hardware security solutions for machine learning
- Secure design for emerging applications: IoT, robotics, wearable computing, etc.
- Architectural designs and hardware security solutions for HPC, Data Centers and cloud computing
- Hardware virtualization and isolation for security
- Hardware-Software co-design solutions: graph analytics,
- Post-quantum cryptosystem designs
- Neuromorphic Architectures
- Blockchain enabled secure computing
- Classic and Modern encryption algorithms and hardware support
- Hardware security support for integrity and authentication, key distribution and management, and trust platform modules
- Secure execution environment
- Memory subsystem organization to secure data accesses
- Network-on-Chip (NoC) security feature to process and compute isolation

Submission Format
1. Oral Presentation Submission: The paper must be submitted in PDF format. The content of the submission is limited to four (4) pages - 8.5"x11" in standard IEEE two-column format (both blind and non-blind submission forms are accepted).
2. Poster Submission: Title and abstract - must be submitted in PDF format.

Deadlines
- Submission: August   26, 2022
- Notification: September   16, 2022
- Final Version: September   30, 2022
- Presentation: October   8, 2022

Submission Site: https://easychair.org/conferences/?conf=secriscv22